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| FORM PTO-1449 INFORMATION DISCLOSURE CITATION | | Atty Docket RAL9-2000-0069-US1 | Serial No. To be assigned |
| | | Applicant Paul D. Bryan, et al. | |
| | | Filing Date *** | Group Art Unit Unassigned |

U.S. PATENT DOCUMENTS

| Examiner Initial | | Document Number | Date | Name | Class | Sub-Class | Filing Date |
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| KDN | AA | 4,958,347 | 09/18/90 | White et al. | 371 | 714 29.543 | PTO |
| KDN | AB | 5,724,504 | 03/03/98 | Aharon et al. | 395 | 714 183.9 33 | |
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|-----|----|-----------------|----------|---------|-------|-----------|--------------|
| KDN | AL | 10340251A | 12/22/98 | JP | G06F | 13/362 | Yes No |
| | AM | | | | | | Yes No |
| | AN | | | | | | Yes No |
| | AO | | | | | | Yes No |
| | AP | | | | | | Yes No |

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

| | | |
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| KDN | AR | IBM Technical Disclosure Bulletin, vol. 24, No. 7A, December 1981, pp3282-3283 "Dynamic Processor Bus LSSD Simulation Modeling Technique" |
| | AS | |
| | AT | |

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| Examiner | <i>W. Thompson</i> | Date Considered 11/26/03 |
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609.

Draw line through citation if not in conformance and not considered.

Include copy of this form with next communication to Applicant.